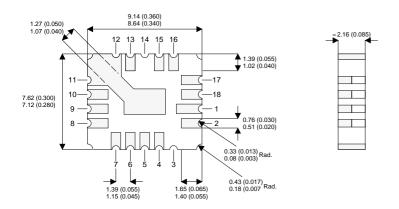




MECHANICAL DATA

Dimensions in mm (inches)



LCC4 CERAMIC SURFACE MOUNT PACKAGE

Underside View

Pads 6, 7, 8, 9, 10, 11, 12, 13. Source Pads 4,5 Gate Pads 1,2,15,16,17,18 Drain

Pads 3,14 **Not Connected**

N-CHANNEL POWER MOSFET

VDSS **500V** I_{D(cont)} 1.5A R_{DS(on)} 3.0Ω

FEATURES

- HERMETICALLY SEALED
- DYNAMIC dv/dt RATING
- AVALANCHE ENERGY RATING
- SIMPLE DRIVE REQUIREMENTS

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

V_{GS}	Gate – Source Voltage	±20V		
I_{D}	Continuous Drain Current $(V_{GS} = 10V, T_{case} = 25^{\circ}C)$	1.5A		
I_{D}	Continuous Drain Current (V _{GS} = 10V , T _{case} = 100°C)	1A		
I_{DM}	Pulsed Drain Current ¹	6.5A		
P_{D}	Power Dissipation @ T _{case} = 25°C	20W		
	Linear Derating Factor	0.16W/°C		
E _{AS}	Single Pulse Avalanche Energy ²	0.11mJ		
dv/dt	Peak Diode Recovery ³	3.5V/ns		
T_J , T_stg	Operating and Storage Temperature Range	-55 to +150°C		
$R_{ heta JC}$	Thermal Resistance Junction to Case	6.25°C/W		
$R_{\theta JCA}$	Thermal Resistance Junction-to-Ambient	175°C/W		
	·	·		

Notes

1) Pulse Test: Pulse Width $\leq 300 \mu s$, $\delta \leq 2\%$

2) @ V_{DD} = 50V , $L \geq 570 \mu H$, R_G = 25Ω , Peak I_L = 14A , Starting T_J = $25^{\circ}C$

3) @ I $_{SD} \le$ 14A , di/dt \le 140A/µs , V $_{DD} \le$ BV $_{DSS}$, T $_{J} \le$ 150°C , Suggested R $_{G}$ = 7.5 Ω

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ELECTRICAL CHARACTERISTICS (T_{amb} = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit	
	STATIC ELECTRICAL RATINGS	I	I					
BV _{DSS}	Drain – Source Breakdown Voltage	V _{GS} = 0	I _D = 1mA	500			V	
ΔBV_{DSS}	Temperature Coefficient of	Reference to 25°C			0.40	,	V/°C	
ΔT_{J}	Breakdown Voltage				0.43			
R _{DS(on)}	Static Drain – Source On–State	V _{GS} = 10V	I _D = 1A			3	Ω	
	Resistance	V _{GS} = 10V	I _D = 1.5A			3.45		
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$	I _D = 250μA	2		4	V	
9 _{fs}	Forward Transconductance	V _{DS} ≥ 15V	I _{DS} = 1A	1			S(\Omega)	
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0$	$V_{DS} = 0.8BV_{DSS}$			25	μΑ	
			T _J = 125°C			250		
I _{GSS}	Forward Gate – Source Leakage	V _{GS} = 20V				100		
I _{GSS}	Reverse Gate – Source Leakage	$V_{GS} = -20V$				-100 nA		
	DYNAMIC CHARACTERISTICS		l				I	
C _{iss}	Input Capacitance	V _{GS} = 0			350			
C _{oss}	Output Capacitance	V _{DS} = 25V			80		pF	
C _{rss}	Reverse Transfer Capacitance	f = 1MHz	ļ		35			
Qg	Total Gate Charge	V _{GS} = 10V	=	7.3		16.7	nC	
0	Cata Sauraa Charga	$V_{DS} = 0.5BV_{DS}$ $I_{D} = 1.5A$		0.1		3		
Q _{gs}	Gate - Source Charge	4 5	-	3.7		8.7	nC	
Q _{gd}	Gate - Drain ("Miller") Charge	$V_{DS} = 0.5BV_{DS}$	3.1		40			
t _{d(on)}	Turn–On Delay Time	$V_{DD} = 250V$ $I_{D} = 1.5A$ $R_{G} = 7.5\Omega$					- - ns -	
t _r	Rise Time					30		
t _{d(off)}	Turn–Off Delay Time					60		
t _f	Fall Time				30			
	SOURCE – DRAIN DIODE CHARAC	TERISTICS	T			4.5		
l _S	Continuous Source Current					1.5	Α	
ISM	Pulse Source Current ²	4.50	T 0500			6.5		
V_{SD}	Diode Forward Voltage	$I_{S} = 1.5A$ $V_{GS} = 0$	$T_J = 25$ °C			1.2	V	
t _{rr}	Reverse Recovery Time	I _F = 1.5A	$T_J = 25^{\circ}C$			900	ns	
Q_{rr}	Reverse Recovery Charge	$d_i / d_t \le 100A/\mu$	s V _{DD} ≤50V			5.9	μС	
t _{on}	Forward Turn-On Time				Negligible			
	PACKAGE CHARACTERISTICS							
L _D	Internal Drain Inductance (from centre of drain pad to die)				5.0		nH	
L _S	Internal Source Inductance (from centre	of source pad to end	of source pad to end of source bond wire)					

Notes 1) Pulse Test: Pulse Width $\leq 300 \mu s$, $\delta \leq 2\%$ 2) Repetitive Rating – Pulse width limited by maximum junction temperature.

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